

5TH INTERNATIONAL CONFERENCE ON INFORMATION SYSTEM DESIGN AND INTELLIGENT APPLICATIONS INDIA (2018)

(19th -21th July, 2018)

Organised by University des Mascareignes, Mauritius

Special Session Timeline

Paper Submission Deadline:
25th May 2018

Notice of Acceptance:
30th May 2018

Final Camera Registration
Submission:
5th June 2018

Conference Date:
19th – 21st July 2018

Paper Submission

All submissions (in word / pdf format) should be mailed with 'INDIA 2018: Name of Special Session: 'mentioned in the subject line directly at the Session Chair E-Mail ID: **preetisudha1@gmail.com**

Publication

All accepted and registered papers of AICC conference will be published in Advances in Intelligent Systems and Computing (AISC) Series, Springer.

Special Session on:

VLSI Design and Embedded Systems (VLSIES'18)

Theme of Session

This special session is basically focused on recent advancement in VLSI and Embedded Systems. In present era the field of electronics & communication plays an important role in almost every sphere of our life. It has penetrated in every field of our existence. It mainly deals with the communication technologies along with the basic electronic components, design of circuits and systems, microprocessors. The advancements in VLSI technology has led path to the historical growth of IC computing power which profoundly changed the way we create, process, communicate, store information and also many latest trends have been introduced by pioneers all around the world.

The Session explores emerging trends and novel ideas and concepts covering a broad range of topics in the area of VLSI: from VLSI circuits, systems and design methods, to system level design and system-on-chip issues, to bringing VLSI methods to new areas and technologies like nano and molecular devices, hardware security, etc.

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Topic of Interest

- System Level Design
- Advances in Digital Design
- Analog and Mixed Signal Design
- Computer Added Design and Verification
- CMOS Technologies and Devices
- FPGA and Reconfigurable Systems
- Embedded Systems and H/S Co-Design
- Wireless Systems design and Security
- Embedded Case Studies
- Low Power Design
- Testing, Reliability and Fault Tolerance

Indexing

The books of this series are submitted to ISI Proceedings, EI-Compendex, DBLP, SCOPUS, Google Scholar and Springerlink.

Please visit conference webpage for registration and other details:

<http://www.indiaconf.org/>

Special Session Chair

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Submission Guidelines for Authors

❖ Prospective authors are invited to submit original research work that falls within the scope of the session. All submissions will be thoroughly peer-reviewed by experts based on originality, significance and clarity.

❖ Only papers presenting novel research results or successful innovative applications will be considered for publication in the conference proceedings.

❖ Kindly ensure that your paper is formatted as per Springer AISC Template (not exceeding 8 pages written in A4 size).

Paper Format

❖ Authors are invited to submit manuscripts written in English.

❖ Prospective authors are invited to submit paper(s) not exceeding 8 pages written in A4 size.

❖ Portable Document Format (pdf) is acceptable.

Springer Policy on Plagiarism

❖ Authors are requested to kindly restrict from plagiarism in any form. Authors should submit their original and unpublished research work not under consideration for publication elsewhere.

❖ Papers found to be plagiarised during any stage of review shall be rejected.

❖ As per copyright transfer agreement, Authors are deemed to be individually and collectively responsible for the content of manuscripts published by them.

❖ Hence, it is the responsibility of each author to strive for the highest ethical standards with respect to plagiarism.

❖ Details of the Plagiarism policy can be referred in Springer link (AISC).